

Laboratory 2

PRELIMINARY Scheduling 2017

Name:

3-Feb-2017

Groupmate:

Course / Group: **d=** H/K **t=** 1/2/3/4 **c=** 1/2/3/4 **g=** **d**ate, **t**ime, **C**ourse, **g**roup

Courses	H41E Monday 17.15-20.00	

Date of measurements

Occasion	Date	Measurement number								Mark
		The last digit of course code and the group number selects the column								
		H41 g0..4	H41 g5..9	H42 g0..4	H42 g5..9	H43 g0..4	H43 g5..9	H44 g0..4	H44 g5..9	
1	6-Feb	2								
2	13-Feb	1								
3	20-Feb	3								
4	27-Feb	8								
5	6-Mar	11								
6	13-Mar	10								
7	20-Mar	4								
8	27-Mar	6								
9	3-Apr	5								
10	10-Apr	7								
11	24-Apr	9								
Rep1-3		Measurement 1. <i>On demand</i>								
		Measurement 2. <i>On demand</i>								
		Measurement 3. <i>On demand</i>								
Rep4-11	2/4-May	Scheduling after finishing the normal measurements								
FM	8-May	Final measurement, on the venue of the measurements								
Rep-FM		Repetition of FM. Sceduling later								
FT		Final theoretical test, see later (5-May or 8-May)								
R-FT		Repetition of theoretical test								

Example: if somebody has course code H4**1**E and group ID **6**2, then he/she should select the column "H4**1** group 5...9", since he/she is in group 6 (the digit "2" in the course code "62" means that the student is the second member of group 6, but it's irrelevant in the scheduling)

Holidays

Date	3. 15.	4. 17.	5. 1.	6. 5.			
Course concerned					days marked with * will be repeated		

The background color of the cells indicates the laboratory room where the measurement is hold (see below)

1. Building and measuring a simple electronic circuit	2. PCB Design	3. Electromagnetic Compatibility (EMC)	107 108 109 110
4. Measuring electrical power BP110	5. Testing basic transistor amplifiers BP109	6. Testing instrumentation amplifiers BP109	7. A/D and D/A converters BP108
8. Identification and control of linear systems BP107	9. Study of Analog Phase-Locked Loop (APLL) BP108	10. Eval. of a 915-MHz FSK SoC Radio Transceiver BP110	11. Application Techniques of Logic Controllers BP107